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Examiner Name

Attorney Docket Number

HRL128

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Application Numb r	10/685,352		
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First Named Inventor	Valley		
Art Unit			
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	1	Y. Ahmed, A. Opal, "An efficient simulation method for oversampled delta-sigma modulators," Proc. of the 37th Midwest Symp. on Circuits and Systems, vol. 2, 1994, pp.1164-1167		
	2	A. Opal, "Sampled data simulation of linear and nonlinear circuits," IEEE Trans. CAD Integrated Circuits and Syst., vol. 15, no. 3, Mar. 1996, pp. 295-307		
	3	Y. Dong, et al, "Time-domain ther. noise sim. of switched capacitor circ. and delta-sigma modulators," IEEE Trans. CAD Int. Circ. and Syst., vol.19, no.4, Apr.2000, pp.473-481		
	4	R. Schreier and B. Zhang, "Delta-Sigma modulators employing continuous-time circuitry," IEEE Trans. Circuits and Syst. I, vol. 43, no. 4, Apr. 1996, pp. 324-332		
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	7	A.P.S. Meliopoulos and CH. Lee, "An alternative method for transient analysis via wavelets," IEEE Trans. Power Delivery, vol. 15, no. 1, Jan. 2000, pp. 114-121		
	8	A.W. Galli, "Discussion of 'An alternative method for transient analysis via wavelets," Ibid, no. 4, Oct. 2000, p. 1326		
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Filing Date	10/14/2003	
First Named Inventor	Valley	
Art Unit		
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Attorney Docket Number	HRL128	
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	11	H.L. Resnikoff and R.O. Wells, Jr., "Wavelet Analysis," Springer, New York, 1998, pp. 236-265	
	12	H.L. Resnikoff and R.O. Wells, Jr., "Wavelet Analysis," Springer, New York, 1998, pp. 281 -340.	
	13	W.H. Press, et al., "Numerical Recipes in Fortran, the Art of Scientific Computing," 2nd Ed., Cambridge University Press, 1992, pp. 340-386	
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